INDEPENDENT SYSTEMS

Self-Compiling Silicon Computer

Discovers actual silicon capability/health and recompiles the logical machine around available resources.

SYSTEM-01 · Integrated reference · System architecture + source

Function and architecture

- Live-PDK state
- resource catalog
- assignment compiler
- link compiler
- memory placement
- power orchestration

Integration summary

Integration scope, interfaces and qualification are agreed for the selected release.

Technical record

Canonical identity	SYSTEM-01
Responsibility	Discovers actual silicon capability/health and recompiles the logical machine around available resources.
Delivery boundary	System architecture + source
Evidence scope	Integrated reference

Delivery and release binding

- Constituent release manifest
- Topology and physical integration assumptions
- System software and orchestration boundaries
- Qualification scope and system-level acceptance

Confirm for the selected release: Environment and dependencies, Version / build hash, API compatibility, License and support scope.

Evidence and qualification

The R2 record contains separate domain tiles, fabric, SoC and chip-top identities, with reference/invariant campaigns and static release audits.

The system source/reference release is not a physical machine delivery or new signoff-class silicon.

Evaluation and licensing

Evaluation: System architecture evaluation. Production: Platform integration program. Custom work: Customer-specific system composition. Rights, pricing, support and acceptance criteria are agreed for the exact scope.

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