OMNIFRAME Ω SOCS

Ω-HYP / Hypha

Fabric switch / chiplet

IP-4928 · Integrated reference · SoC architecture + source

Function and architecture

- adaptive OMF routing
- deterministic emergency lanes
- time sync
- quarantine
- telemetry

Integration summary

Integration scope, interfaces and qualification are agreed for the selected release.

Technical record

Canonical identity	IP-4928
Responsibility	Fabric switch / chiplet
Delivery boundary	SoC architecture + source
Evidence scope	Integrated reference

Delivery and release binding

- Constituent release manifest
- Topology and physical integration assumptions
- System software and orchestration boundaries
- Qualification scope and system-level acceptance

Confirm for the selected release: Environment and dependencies, Version / build hash, API compatibility, License and support scope.

Evidence and qualification

The current estate records a structurally closed 17-SoC hierarchy and native software/reference-model evidence.

Inherited structural RTL and model evidence do not constitute a fresh foundry physical-signoff or fabricated-product result.

Evaluation and licensing

Evaluation: System architecture evaluation. Production: Platform integration program. Custom work: Customer-specific system composition. Rights, pricing, support and acceptance criteria are agreed for the exact scope.

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