MEMORY ENGINEERING

OMNIMEM

Family-level generated-memory architecture spanning selectable bitcells, banking, periphery, power/test/reliability contracts and physical handoff.

IP-4043 · Source-backed architecture · Compiler architecture + source scope

Function and architecture

- Family-level generated-memory architecture spanning selectable bitcells
- banking
- periphery
- power/test/reliability contracts and physical handoff

Integration summary

OMNIMEM: Family-level generated-memory architecture spanning selectable bitcells, banking, periphery, power/test/reliability contracts and physical handoff. Delivered as Compiler architecture + source scope. Integrate the selected version at its documented input/output boundary; retain source and configuration identity with each result. The exact callable API, dependencies and runtime environment are supplied with the controlled release; this public description is not a substitute for that release contract.

Technical record

Canonical identity	IP-4043
Responsibility	Family-level generated-memory architecture spanning selectable bitcells, banking, periphery, power/test/reliability contracts and physical handoff.
Delivery boundary	Compiler architecture + source scope
Evidence scope	Source-backed architecture
Engineering boundary	Family-level generated-memory architecture spanning selectable bitcells, banking, periphery, power/test/reliability contracts and physical handoff.
Deployment	Compiler architecture + source scope;
Operational scope	The named architecture and its implementation scope are not a blanket shipping-product, physical qualification or production-deployment claim. The exact release controls evaluation.

Delivery and release binding

- Versioned package and dependency lock
- Deployment environment and access controls

- Input/output schemas and integration examples
- Evaluation scope, support and update policy

Confirm for the selected release: Environment and dependencies, Version / build hash, API compatibility, License and support scope.

Evidence and qualification

P1 / SOURCE-BACKED ARCHITECTURE; EXECUTED HDL/PHYSICAL OPEN

The named architecture and its implementation scope are not a blanket shipping-product, physical qualification or production-deployment claim. The exact release controls evaluation.

Canonical status: P1 / SOURCE-BACKED ARCHITECTURE; EXECUTED HDL/PHYSICAL OPEN

Evaluation and licensing

Evaluation: Evaluate the named software release. Production: Team or enterprise deployment. Custom work: Workflow and API integration. Rights, pricing, support and acceptance criteria are agreed for the exact scope.

Contact: patrick@garmolabs.com | Product path: /software/omnimem