FABRIC & CONNECTIVITY

GHSI Reliable Link IP v1.0

Digital reliability layer implementing CRC32, sequencing, retained retry state, ACK/NACK behavior, deterministic striping and single-spare substitution.

IP-4281 · RTL + models · Soft IP / model

Function and architecture

- Digital reliability layer implementing CRC32
- sequencing
- retained retry state
- ACK/NACK behavior
- deterministic striping and single-spare substitution

Integration summary

GHSI Reliable Link IP v1.0 addresses digital reliability layer implementing CRC32, sequencing, retained retry state, ACK/NACK behavior, deterministic striping and single-spare substitution.

Technical record

Canonical identity	IP-4281
Responsibility	Digital reliability layer implementing CRC32, sequencing, retained retry state, ACK/NACK behavior, deterministic striping and single-spare substitution.
Delivery boundary	Soft IP / model
Evidence scope	RTL + models

Delivery and release binding

- Interface version and parameter set
- Clock/reset domains and integration constraints
- Companion memory, PHY and software requirements
- Verification collateral and acceptance criteria

Confirm for the selected release: Node/library binding, Clock/reset limits, Physical and protocol dependencies, Artifact hashes, Acceptance criteria.

Evidence and qualification

Our records show source/model or productization evidence for this IP. The selected HDL, verification and physical views are reviewed per release.

No node-port, silicon-performance, standards certification or analog qualification is inherited from a related product.

Canonical status: P1-P2 / RTL SOURCE + MODEL; REMOTE HDL NOT_RUN; NO ANALOG RATE CLAIM

Evaluation and licensing

Evaluation: Evaluate an individual IP configuration. Production: Named-design production license. Custom work: Node binding or derivative integration. Rights, pricing, support and acceptance criteria are agreed for the exact scope.

Contact: patrick@garmolabs.com | Product path: /ip/ghsi-reliable-link-ip-v1-0