MEMORY & STORAGE

KENBE 512-bit XOR Reduction Near-Memory

512-bit XOR/reduction datapath for guarded near-memory work.

IP-4011 · RTL + models · Soft IP / model

Function and architecture

• 512-bit XOR/reduction datapath for guarded near-memory work

Integration summary

KENBE 512-bit XOR Reduction Near-Memory addresses 512-bit XOR/reduction datapath for guarded near-memory work.

Technical record

Canonical identity	IP-4011
Responsibility	512-bit XOR/reduction datapath for guarded near-memory work.
Delivery boundary	Soft IP / model
Evidence scope	RTL + models

Delivery and release binding

• Interface version and parameter set

• Clock/reset domains and integration constraints

• Companion memory, PHY and software requirements

• Verification collateral and acceptance criteria

Confirm for the selected release: Node/library binding, Clock/reset limits, Physical and protocol dependencies, Artifact hashes, Acceptance criteria.

Evidence and qualification

Our records show source/model or productization evidence for this IP. The selected HDL, verification and physical views are reviewed per release.

No node-port, silicon-performance, standards certification or analog qualification is inherited from a related product.

Canonical status: P1/P2 / SOURCE-CONTAINED RTL

Evaluation and licensing

Evaluation: Evaluate an individual IP configuration. Production: Named-design production license. Custom work: Node binding or derivative integration. Rights, pricing, support and acceptance criteria are agreed for the exact scope.

Contact: patrick@garmolabs.com | Product path: /ip/kenbe-512-bit-xor-reduction-near-memory