SECURITY & CRYPTOGRAPHY

SKYRA Security Island Architecture

Composite security model spanning native capability checks, TEE isolation, secure/measured boot root, memory-encryption boundary, epoch revocation, and side-channel hooks.

IP-2588 · Source / reference evidence · RTL / reference evaluation

Function and architecture

• Composite security model spanning native capability checks, TEE isolation, secure/measured boot root, memory-encryption boundary, epoch revocation, and side-channel hooks.

Integration summary

SKYRA Security Island Architecture provides the documented security architecture responsibility: Composite security model spanning native capability checks, TEE isolation, secure/measured boot root, memory-encryption boundary, epoch revocation, and side-channel hooks. Evaluate the exact SKYRA-IP-015 implementation and confirm its integration contract before using it inside the target design.

Technical record

Canonical identity	IP-2588
Native implementation identity	SKYRA-IP-015
Documented responsibility	Composite security model spanning native capability checks, TEE isolation, secure/measured boot root, memory-encryption boundary, epoch revocation, and side-channel hooks.
Recorded evidence class	Source / reference evidence
Interface and physical parameters	Bound to the selected source/configuration; not inferred from name or family.

Delivery and release binding

• Exact implementation and configuration manifest

• Documented interfaces and clock/reset contract

• Available reference or verification collateral

• Recorded physical views where authorized and applicable

• Integration acceptance criteria and unresolved gate list

Confirm for the selected release: Source/configuration identity, Interface and dependencies, Verification evidence, Process/library where applicable, Authorized package and acceptance criteria.

Evidence and qualification

Security RTL set + capability RTL + architecture corrections.

Evidence shown is our record for this canonical implementation, not a fresh tool rerun. External foundry acceptance, silicon measurements, standards certification and unlisted interface/physical parameters are not inferred. Exact delivery and rights are agreed before licensing.

Canonical status: P1 / IMPLEMENTATION-BACKED ARCHITECTURE; SECURITY VALIDATION OPEN

Evaluation and licensing

Evaluation: Review the exact recorded implementation and agreed test scope. Production: Named-design rights for the agreed qualified release. Custom work: Target integration, verification or physical qualification. Rights, pricing, support and acceptance criteria are agreed for the exact scope.

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