MEMORY & STORAGE

FPP Package Cell

Compute/memory/I/O/power/fabric/management pod + local bridge/RDL + power/thermal/mechanical interfaces

IP-158 · Architecture IP · Architecture

Function and architecture

- Compute, memory, I/O, power and management pod composition
- Local bridge and redistribution-layer integration
- Power, thermal and mechanical package interfaces

Integration summary

FPP Package Cell integration role: Repeatable package scaling unit. Documented composition: Compute/memory/I/O/power/fabric/management pod + local bridge/RDL + power/thermal/mechanical interfaces.

Technical record

Canonical identity	IP-158
Responsibility	Repeatable package scaling unit
Documented building blocks	Compute/memory/I/O/power/fabric/management pod + local bridge/RDL + power/thermal/mechanical interfaces
Delivery boundary	Architecture
Evidence scope	Architecture IP

Delivery and release binding

- Interface version and parameter set
- Clock/reset domains and integration constraints
- Companion memory, PHY and software requirements
- Verification collateral and acceptance criteria

Confirm for the selected release: Node/library binding, Clock/reset limits, Physical and protocol dependencies, Artifact hashes, Acceptance criteria.

Evidence and qualification

Canonical technology and its architecture/integration role are recorded. The offered delivery is confirmed against the exact release before licensing.

No node-port, silicon-performance, standards certification or analog qualification is inherited from a related product.

Canonical status: Architecture

Evaluation and licensing

Evaluation: Evaluate an individual IP configuration. Production: Named-design production license. Custom work: Node binding or derivative integration. Rights, pricing, support and acceptance criteria are agreed for the exact scope.

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