INTEGRATION & TOOLING

GHK — Hard-IP Kit

Portable APIs to memory, clock, I/O, power, sensor, security, analog, D2D, ESD and package IP

IP-137 · Architecture IP · Architecture

Function and architecture

- Portable APIs to memory
- clock
- power
- sensor
- security
- analog
- ESD and package IP

Integration summary

GHK — Hard-IP Kit integration role: Process-specific hard-IP layer. Documented composition: Portable APIs to memory, clock, I/O, power, sensor, security, analog, D2D, ESD and package IP.

Technical record

Canonical identity	IP-137
Responsibility	Process-specific hard-IP layer
Documented building blocks	Portable APIs to memory, clock, I/O, power, sensor, security, analog, D2D, ESD and package IP
Delivery boundary	Architecture
Evidence scope	Architecture IP

Delivery and release binding

- Interface version and parameter set
- Clock/reset domains and integration constraints
- Companion memory, PHY and software requirements
- Verification collateral and acceptance criteria

Confirm for the selected release: Node/library binding, Clock/reset limits, Physical and protocol dependencies, Artifact hashes, Acceptance criteria.

Evidence and qualification

Canonical technology and its architecture/integration role are recorded. The offered delivery is confirmed against the exact release before licensing.

No node-port, silicon-performance, standards certification or analog qualification is inherited from a related product.

Canonical status: Master build spec

Evaluation and licensing

Evaluation: Evaluate an individual IP configuration. Production: Named-design production license. Custom work: Node binding or derivative integration. Rights, pricing, support and acceptance criteria are agreed for the exact scope.

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