FABRIC & CONNECTIVITY

GUT — Universal Transaction

Memory, stream, message, atomic, DMA, event, control, discovery, health and trace operations

IP-131 · Architecture IP · Architecture

Function and architecture

- Memory
- stream
- message
- atomic
- event
- control
- discovery

Integration summary

GUT — Universal Transaction integration role: Semantic normalization layer. Documented composition: Memory, stream, message, atomic, DMA, event, control, discovery, health and trace operations.

Technical record

Canonical identity	IP-131
Responsibility	Semantic normalization layer
Documented building blocks	Memory, stream, message, atomic, DMA, event, control, discovery, health and trace operations
Delivery boundary	Architecture
Evidence scope	Architecture IP

Delivery and release binding

- Interface version and parameter set
- Clock/reset domains and integration constraints
- Companion memory, PHY and software requirements
- Verification collateral and acceptance criteria

Confirm for the selected release: Node/library binding, Clock/reset limits, Physical and protocol dependencies, Artifact hashes, Acceptance criteria.

Evidence and qualification

Canonical technology and its architecture/integration role are recorded. The offered delivery is confirmed against the exact release before licensing.

No node-port, silicon-performance, standards certification or analog qualification is inherited from a related product.

Canonical status: Architecture

Evaluation and licensing

Evaluation: Evaluate an individual IP configuration. Production: Named-design production license. Custom work: Node binding or derivative integration. Rights, pricing, support and acceptance criteria are agreed for the exact scope.

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