INTERFACES & MIXED-SIGNAL

GCE — Continuum Element

LUT/FF/SRAM + GM/CAP/CMP/SW/BIAS + routing + config + BIST/repair/health

IP-123 · Architecture IP · Architecture

Function and architecture

- LUT, flip-flop and SRAM resources
- Transconductance, capacitor, comparator, switch and bias resources
- Routing and configuration plane
- Built-in test, repair and health functions

Integration summary

GCE — Continuum Element integration role: Homogeneous field cell. Documented composition: LUT/FF/SRAM + GM/CAP/CMP/SW/BIAS + routing + config + BIST/repair/health.

Technical record

Canonical identity	IP-123
Responsibility	Homogeneous field cell
Documented building blocks	LUT/FF/SRAM + GM/CAP/CMP/SW/BIAS + routing + config + BIST/repair/health
Delivery boundary	Architecture
Evidence scope	Architecture IP

Delivery and release binding

- Interface version and parameter set
- Clock/reset domains and integration constraints
- Companion memory, PHY and software requirements
- Verification collateral and acceptance criteria

Confirm for the selected release: Node/library binding, Clock/reset limits, Physical and protocol dependencies, Artifact hashes, Acceptance criteria.

Evidence and qualification

Canonical technology and its architecture/integration role are recorded. The offered delivery is confirmed against the exact release before licensing.

No node-port, silicon-performance, standards certification or analog qualification is inherited from a related product.

Canonical status: Master architecture

Evaluation and licensing

Evaluation: Evaluate an individual IP configuration. Production: Named-design production license. Custom work: Node binding or derivative integration. Rights, pricing, support and acceptance criteria are agreed for the exact scope.

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