MEMORY & STORAGE

GFI-Mem

Versioned memory-service semantics alongside GFI-Lite/Stream/DMA/Event/Trace

IP-120 · Architecture IP · Architecture

Function and architecture

• Versioned memory-service semantics

• Companion boundary to GFI-Lite, Stream, DMA, Event and Trace

Integration summary

GFI-Mem integration role: Memory service interface. Documented composition: Versioned memory-service semantics alongside GFI-Lite/Stream/DMA/Event/Trace.

Technical record

Canonical identity	IP-120
Responsibility	Memory service interface
Documented building blocks	Versioned memory-service semantics alongside GFI-Lite/Stream/DMA/Event/Trace
Delivery boundary	Architecture
Evidence scope	Architecture IP

Delivery and release binding

• Interface version and parameter set

• Clock/reset domains and integration constraints

• Companion memory, PHY and software requirements

• Verification collateral and acceptance criteria

Confirm for the selected release: Node/library binding, Clock/reset limits, Physical and protocol dependencies, Artifact hashes, Acceptance criteria.

Evidence and qualification

Canonical technology and its architecture/integration role are recorded. The offered delivery is confirmed against the exact release before licensing.

No node-port, silicon-performance, standards certification or analog qualification is inherited from a related product.

Canonical status: Architecture contract

Evaluation and licensing

Evaluation: Evaluate an individual IP configuration. Production: Named-design production license. Custom work: Node binding or derivative integration. Rights, pricing, support and acceptance criteria are agreed for the exact scope.

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