FABRIC & CONNECTIVITY

GFI-DMA

Local/remote memory/IP movement; queues; chaining; completion/error

IP-108 · Architecture IP · Architecture

Function and architecture

- Local/remote memory/IP movement
- queues
- chaining
- completion/error

Integration summary

GFI-DMA integration role: Garmo descriptor DMA. Documented composition: Local/remote memory/IP movement; queues; chaining; completion/error.

Technical record

Canonical identity	IP-108
Responsibility	Garmo descriptor DMA
Documented building blocks	Local/remote memory/IP movement; queues; chaining; completion/error
Delivery boundary	Architecture
Evidence scope	Architecture IP

Delivery and release binding

- Interface version and parameter set
- Clock/reset domains and integration constraints
- Companion memory, PHY and software requirements
- Verification collateral and acceptance criteria

Confirm for the selected release: Node/library binding, Clock/reset limits, Physical and protocol dependencies, Artifact hashes, Acceptance criteria.

Evidence and qualification

Canonical technology and its architecture/integration role are recorded. The offered delivery is confirmed against the exact release before licensing.

No node-port, silicon-performance, standards certification or analog qualification is inherited from a related product.

Canonical status: Reusable block/interface IP

Evaluation and licensing

Evaluation: Evaluate an individual IP configuration. Production: Named-design production license. Custom work: Node binding or derivative integration. Rights, pricing, support and acceptance criteria are agreed for the exact scope.

Contact: patrick@garmolabs.com | Product path: /ip/gfi-dma